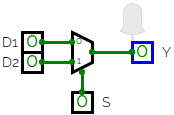
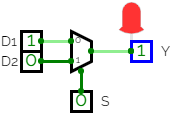
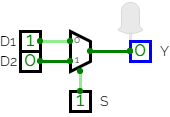
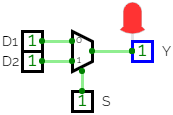
MULTIPLEXER AND DEMULTIPLEXER

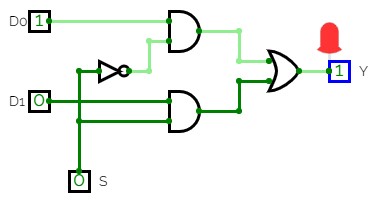
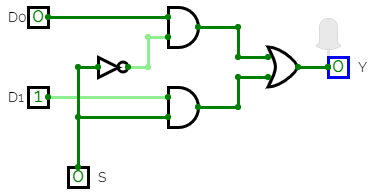
**MULTIPLEXER**

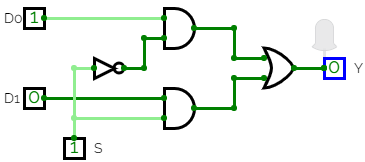
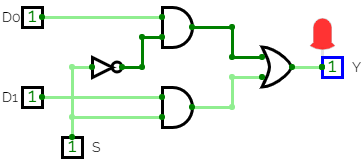
**BLOCK DIAGRAM OF 2:1 MULTIPLEXER:**

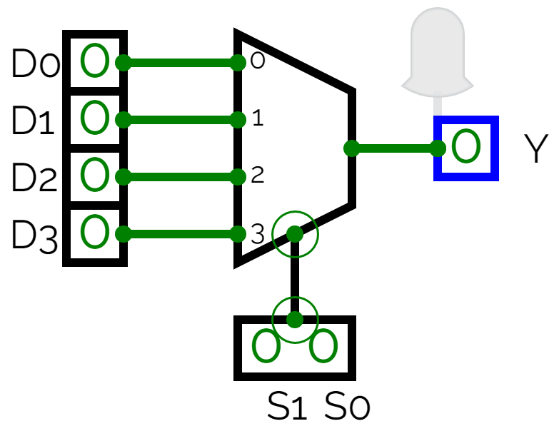
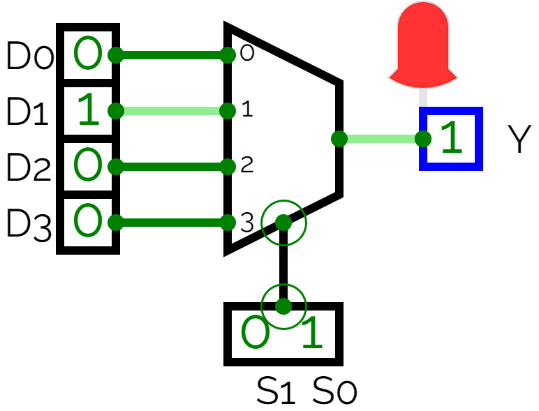
 

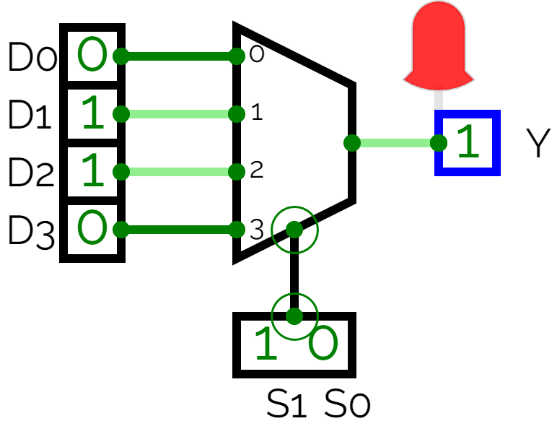
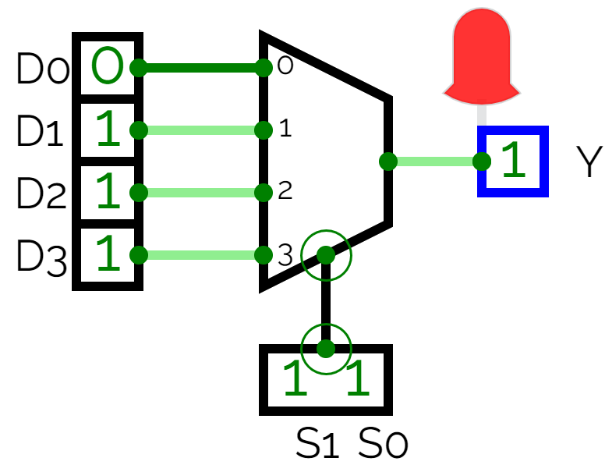
**CIRCUIT DIAGRAM OF 2:1 MULTIPLEXER:**



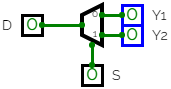
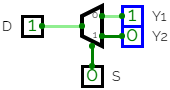
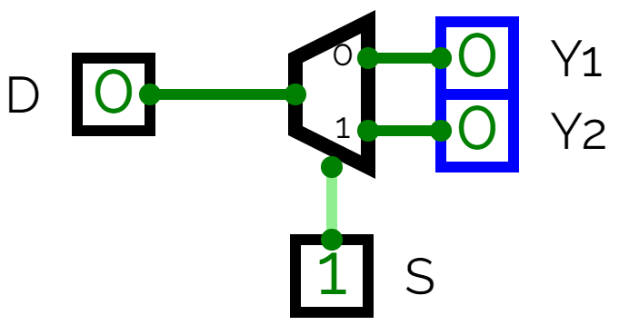
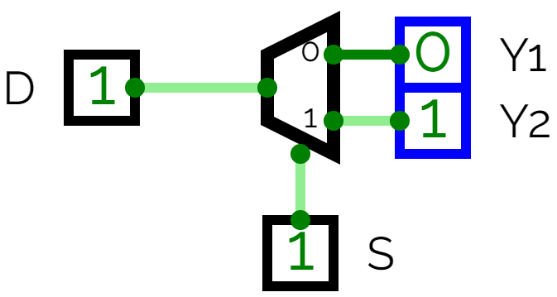
**BLOCK DIAGRAM OF 4:1 MULTIPLEXER:**

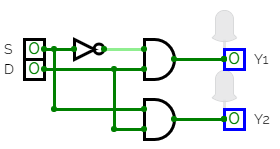
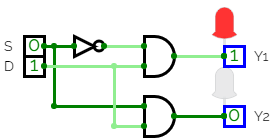
 

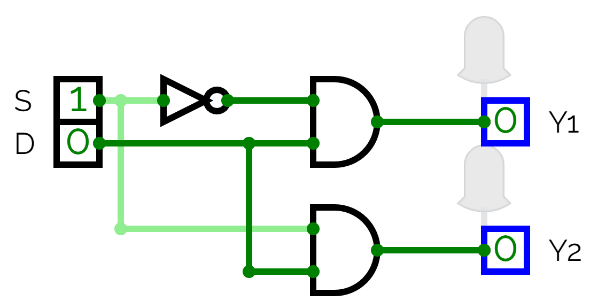
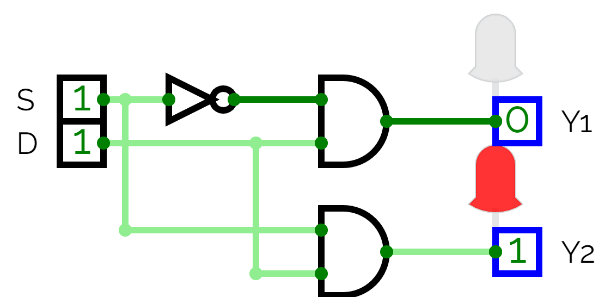
DEMULTIPLEXER

**BLOCK DIAGRAM OF 2:1 DEMULTIPLEXER:**

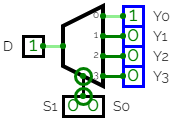
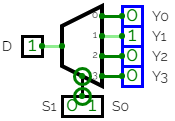
   

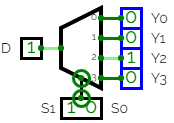
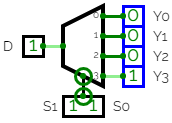
**CIRCUIT DIAGRAM OF 2:1 DEMULTIPLEXER:**

**BLOCK DIAGRAM OF 4:1 MULTIPLEXER:**

HENCE THE MULTIPLEXER AND DEMULTIPLEXER ARE PERFORMED.